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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet

Application No. : 09/816,034

Filing Date : March 22, 2001

First Named Inventor : Kensaku Amou et al.

Group Art Unit :

Examiner Name :

Attorney Docket No. : FUJA 18.500

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document	Kind Code if known ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document Office ³ Number ⁴ Kind Code ⁵ (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
<i>lu</i>		11-41241	JP	Hitachi Ltd.	02/12/1999	
<i>lu</i>		11-112527 Appl No. 2959540	JP	NEC Corp.	04/23/1999	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check here if English language translation attached ⁷
<i>lu</i>		"A Generalized Processor Sharing Approach to Flow Control in Integrated Service Networks: The Single-Node Case", June 1993, A.K. Parekh et al, IEEE/ACM Trans. on Networking, Vol. 1, pp. 344-357.	RECEIVED MAY 16 2001 Technology Center 2501C
		"A Self-Clocked Fair Queueing Scheme for Broadband Applications", 1994, S. Golestani, pp. 636-646.	
<i>lu</i>		"Virtual Clock: A New Traffic Control Algorithm for Packet Switching Networks", 1990, L. Zhang, pp. 19-29.	

Examiner Signature	<i>lu</i>	Date Considered	7/24/04
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